

## ■ Mais aussi

↪ **Licences**

↪ **Kits**

- AMS, XFAB, IBM, ST

↪ **Machines**

- **Serveur, Station de travail**
- **PC linux, Sun**

↪ **Formation**

↪ **Hot line**

- **Sites mainteneurs**

## ■ Full Custom

↪ **Schematic**

↪ **Simulation**

- **VerilogXL**
- **Eldo ??**
- **Spectre / Verilog**

↪ **Layout**

- **Virtuoso**
- **VirtuosoXL**
- **Virtuoso Custom Router**

↪ **Diva**

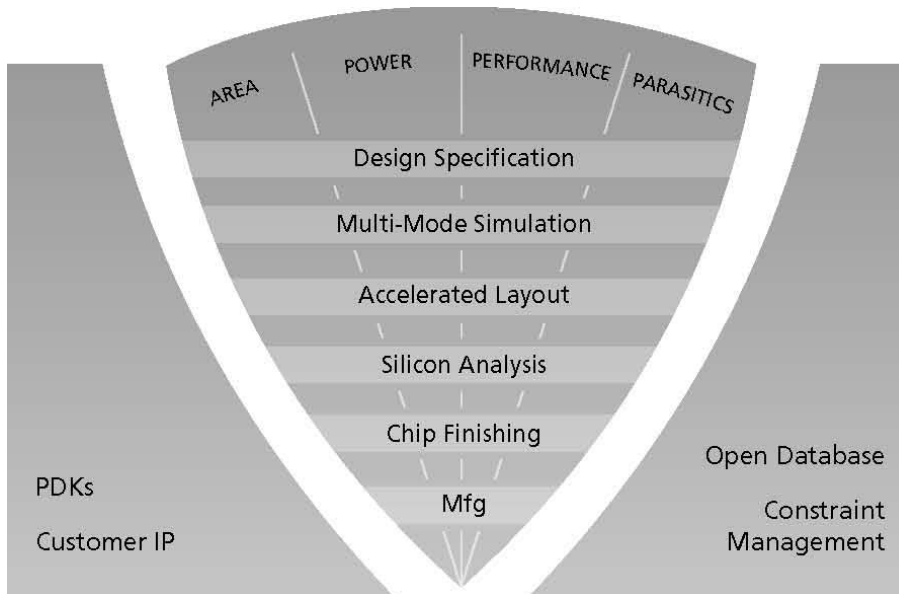
↪ **Assura**

- **DRC / LVS**
- **RCX postsim Ana, Mxt**

## ■ Digital design

↪ **ncVerilog, ncVHDL**

↪ **Ambit, Silicon Ensemble**



## ■ Prochain marché Cadence en 2008

- **Design Spec**
  - ↪ **Spec. Driven Environ.**
  - ↪ **Characterization & modeling**
  - ↪ **Neocircuit**
- **Simulation**
  - ↪ **AMS Designer**
    - VerilogAMS + Spectre
    - Ultrasim / Verilog
- **Layout**
  - ↪ **V + VXL**
  - ↪ **Virtuoso Custom Router**
- **Analysis**
  - ↪ **Assura**
    - DRC / LVS
    - RCX postsim Ana, Mxt
  - ↪ **IR Drop**
  - ↪ **Electromigration**