





### A Charge-Sensitive Amplifier Associated with APD or PMT for Positron Emission Tomography Scanners

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# Outline

- 1. PET scanners and electronic architecture
- 2. Charge Sensitive Amplifier design
- 3. Noise considerations
- 4. Layout design
- 5. Test set up and measurements
- 6. Conclusion



# 1: PET Scanners Concept

Sensitive instruments for biomedical imaging :

- Brain studies
- Cardiac imaging
- Cancer diagnosis and therapy

PET scanners operation :

• Detection in coincidence of 511 KeV Photon pair



<u>Main ideas :</u> o Use LHC (CERN) electronics concepts o Dead time reduction << 1ns o Spatial resolution ~ 1mm o Improvements of PET performances



## 1: Electronic Architecture





CSA is made with a fully differential amplifier = Adm + CMFB Technology : AMS 0.35 $\mu$ m BiCMOS SiGE S35D4M5 4 metal, 2 poly, high resistive poly





### 2 : Charge Sensitive Amplifier Design



CMFB part

$$p_{nd} = \frac{g_{m25}}{C_{gs5} + C_{gs25}}$$

lds25/lds5 ~ 1

### CSA input characteristics

$$\tau_{csa} = \frac{C_d}{2\pi \cdot GBW \cdot C_f} = C_d \cdot R_{in}$$

$$R_{in} = \frac{C_{out}}{g_{m1} \cdot C_f}$$



### 3 : Noise considerations

 $S_n =$ 

(Noise is a Key issue for this design)

$$\frac{8}{3}\frac{kT}{g_{m1}} + \frac{K_f}{C_{ox}^2WLf} \qquad Cf < Cp \text{ and } Cf < Cd$$

$$\Delta V_{out}(\omega) = \frac{2A_{dm}\left[(v_1 - v_2) - V_{out,cm}(\beta_1 - \beta_2)\right]}{2 + A_{dm}(\beta_1 + \beta_2)}$$

$$\frac{1}{j\omega C_p} \qquad 1 + j\omega C_f R_f \qquad \frac{1}{j\omega C_r} \qquad 1 + j\omega C_f R_f$$

$$\beta_{1} = \frac{\frac{1}{j\omega C_{p}}}{\frac{1}{j\omega C_{p}} + \frac{R_{f}}{1 + j\omega R_{f}C_{f}}} \sim \frac{1 + j\omega C_{f}R_{f}}{1 + j\omega C_{p}R_{f}} \qquad \beta_{2} = \frac{\frac{1}{j\omega C_{d}}}{\frac{1}{j\omega C_{d}} + \frac{R_{f}}{1 + j\omega R_{f}C_{f}}} \sim \frac{1 + j\omega C_{f}R_{f}}{1 + j\omega C_{d}R_{f}}$$

$$H_{n}(\omega) \approx \frac{\Delta Vout}{V_{1,2}} \approx \frac{2}{\beta_{1} + \beta_{2}} \approx \frac{2}{\frac{1 + j\omega C_{f}R_{f}}{1 + j\omega C_{d}R_{f}}} \approx \frac{2}{1 + j\omega C_{f}R_{f}} \approx \frac{2}{C_{f}(\frac{1}{C_{d}} + \frac{1}{C_{p}})} \approx \frac{2C_{d}}{C_{f}} \qquad \text{Noise Gain} \qquad H_{n}(\omega) \approx \frac{C_{d}}{C_{f}}$$

$$v_{noise}^{2} \approx \int_{0}^{\infty} 2S_{n} |H_{n}(f)|^{2} \frac{G^{2} (2\pi f \tau_{s})^{2}}{\left[1 + (2\pi f \tau_{s})^{2}\right]^{2}} df \approx \frac{G^{2}}{\tau_{s}} \left(\frac{2}{C_{f} \left(\frac{1}{C_{d}} + \frac{1}{C_{p}}\right)}\right)^{2} \frac{2kT}{3g_{m1}} \approx \frac{G^{2}}{\tau_{s}} \left(\frac{2C_{d}}{C_{f}}\right)^{2} \frac{2kT}{3g_{m1}}$$



$$V_{out},_{shaper}(\tau_s) = 2 \cdot \frac{qG}{e.C_f}$$

In differential mode

$$ENC \approx \frac{eC_d}{q} \sqrt{\frac{2kT}{3\tau_s g_{m1}}}$$

$$ENC \approx \frac{eC_d}{q} \sqrt{\frac{kT}{\tau_s} \left(\frac{2}{3g_{m1}} + r_{poly}\right)}$$

Serial noise only

Including access gate serial resistor r<sub>poly</sub>

PMOS input Optimization :

- Increase Gm (100 mA/V @ Id 10 mA and w/I =5000/0.35)
- Minimize WL to minimize Cgs input transistor capacitance
- Minimize in layout access gate serial resistor

# 3 : Noise considerations

# Differential output

#### Single ended output



- ENC increase by a factor sqrt(2)
- Adaptation to either PMT or APD
- Less sensitive to coupling (common mode)



4 : Layout Design



Special thanks to E. Bechetoille for his help



# 4 : Layout Design

- Chip was submitted in MPW in November 2008 through CMP
- Return in February 2009 and fully tested
- 28 pins JLCC ceramic package



#### **Photography**



Core cell area = 0.2 mm2



### 5 : Test setup





## 5 : Results









outPA1, outPA2, 3 chips fully tested from 50fC to 700fC in pulsed mode

Ecart = f (Qin) 10 5 Vout1 \_PA chip1 Vout2 PA chip1 0 700 Vout1\_PA 600 800 Ecart (%) chip2 Vout2 PA -5 chip2 chip3 -10 Vout2 PA chip3

Nonlinearity





## 5 : Results





Noise Results :

ENC = 2000 e-Cd = 50pF Gconv : estimated with pulsed stimulus (~ 40 mV/fC) Shaper time constant : 20ns

$$ENC_{meas} = 692 e + 25 e - /pF$$
  
 $ENC_{sim} = 670 e + 21 e - /pF$ 



# 6 : Conclusion

- o Proceeding conference MIPRO (Croatia may 2009)
- o Paper submitted in MIM-A

We designed a CSA in differential mode (APD, PM) which is adapted to the post processing (differential shaper and ADC)

Results are (3 chips were measured):

- ✓ Rise time 26ns for pulsed stimulus
- ✓ Rise time ~ 110ns for APD like stimulus
- ✓ Noise 2000 e- @ Cd=50pF, pulsed stimulus, 20ns shaper time constant
- ✓ Noise 3200 e- @ Cd=50pF, APD stimulus, 20ns shaper time constant
- ✓ Dynamic range 700fC
- ✓ Conversion gain 1.34mV/fC
- Area = 0.195 mm2 not including pad ring
- $\checkmark$  Power supply = 136 mW @ 3.5V